Amendments to the Claims:

Following is a complete listing of the claims pending in the application, as amended:

1.   (Currently amended) In a computing network having at least two data storage or processing devices, Aa method performed by a computer system for providing a complete end-to-end data path verification in thea computer system, the computer system comprising at least a first physical data path and a second physical data path from a processor of the computer system to a data receiver of the computer system, the steps method comprising:

> a)————providing a data initiator within said the computer system, the data initiator comprising the processor, wherein the processor is coupled to the first and second physical data paths, and wherein the first and second physical data paths within the computer system are configured to be coupled to the at least two data storage or processing devices in the computing network and external to the computer system;

> b)———providing a data receiver within said computer system and operatively connected to said data initiator over a first data path;

> c)————transmitting a predetermined data test pattern from thesaid data initiator processor to the said data receiver of the computer system over said the first physical data path, wherein the first physical data path is within the computer system;

> d)————requesting from the data receiver the receiving said transmitted predetermined data test pattern that the data receiver received from the data initiator from step (c) at said data receiver;

> e)————transmitting reading the said received data test pattern from step (d) from thesaid data receiver back to said data initiator over a the second physical data path, wherein the second physical path is within the computer system; and

> f)————receiving said data test pattern of step (e) at said data initiator; and

g)————comparing these~~said~~ predetermined data test pattern ~~of step (e)~~ transmitted to the data receiver over the first physical data path with these~~said~~ data test pattern ~~of step (f)~~read from the data receiver over the second physical data path.

2.   (Currently amended)   The method for providing a complete end-to-end data path verification in a computer system having at least a first physical data path and a second physical data path, as recited in claim 1, the ~~steps~~ method further comprising:

h)————indicating an error condition when these~~said~~ predetermined data pattern transmitted to the data receiver over the first physical data path ~~of step (e)~~ and these~~said~~ data test pattern ~~received~~ read from the data receiver over the second physical data path ~~said data receiver if step (f)~~ are different.

3.   (Cancelled)

4.   (Currently amended)   The method for providing a complete end-to-end data path verification in a computer system having at least a first physical data path and a second physical data path, as recited in claim 1, wherein these~~said~~ computer system comprises one of the group: a computer and storage router.

5.   (Cancelled)

6.   (Cancelled)

7.   (Currently amended)   The method for providing a complete end-to-end data path verification in a computer system having at least a first physical data path and a second physical data path, as recited in claim ~~6~~4, wherein these~~said~~ computer system further comprises a ~~at least one from the group of devices:~~ PCI bus and PCI bridge~~, SCSI controller, SCSI interface, fibre channel controller, fibre channel interface~~.

8.   (Currently amended)   The method for providing a complete end-to-end data path verification in a computer system having at least a first physical data path and a second physical data path, as recited in claim 7, wherein thesaid complete end-to-end data path comprises a physical connection at least one from said group of devices: PCI bus and PCI bridge, SCSI controller, SCSI interface, fibre channel controller, with a memory controllerfibre channel interface.

9.   (Cancelled)

10.   (Currently amended)   The method for providing a complete end-to-end data path verification in a computer system having at least a first physical data path and a second physical data path, as recited in claim 2, wherein at least one of said the transmitting, step (c), said receiving requesting, reading,step (d), said transmitting step (e) and said receiving step (f) and comparing are repeated periodically.

11.   (Cancelled)

12.   (Currently amended)   The method for providing a complete end-to-end data path verification in a computer system having at least a first physical data path and a second physical data path, as recited in claim 2, wherein thesaid data receiver comprises at least one of the devices: a SCSI controller, a memory controller, a fibre channel controller.

13. (New) A data storage router having at least two physical data paths for providing a complete end-to-end data path verification in a computer network, the at least two physical data paths comprising a first data path and a second data path, the computer network comprising the storage router and at least two data storage or processing devices, the data storage router coupled to the at least two data storage or processing devices, the data storage router comprising:

    a processor that creates a data test pattern, the data test pattern created to exercise all data lines in the first and second data paths in a high state and a low state;

    a buffer memory for storing a data test pattern;

    a first data path for transferring within the data storage router the data test pattern from the processor to the memory within the data storage router;

    a second data path within the data storage router from the processor to the memory within the data storage router for verifying the transferred data test pattern; and

    wherein the first and second data paths are configured to be able to be coupled to the at least two data storage or processing devices external to the data storage router.

14. (New) The data storage router of claim 13 wherein a memory controller is operatively coupled to the first and second data paths.

15. (New) The data storage router of claim 14 wherein the first path includes a physical coupling between the processor and the memory controller and the second data path includes a PCI bus to which the processor and memory controller are operatively coupled.

16. (New) The data storage router of claim 14 wherein the first data path includes a PCI bus to which the processor and memory controller are coupled and the

second data path includes a physical coupling between the processor and the memory controller.

17.   (New) The data storage router of claim 13 wherein the first data path includes a first PCI bus and the second data path includes a second PCI bus.

18.   (New) The data storage router of claim 13 wherein the processor transfers the data test pattern periodically.

19.   (New) The data storage router of claim 13 wherein the processor transfers the data test pattern when a host performs an ending action.

20.   (New) The data storage router of claim 13 wherein the data test pattern is separately transferred and verified using both physical data paths.